# **On Riemannian quantum circuit optimization for fermionic Hamiltonian simulation**



**Isabel Nha Minh Le<sup>1</sup> and Christian B. Mendl<sup>1,2</sup>** <sup>1</sup> Technical University of Munich , School of Computation, Information and Technology <sup>2</sup> Technical University of Munich, Institute for Advanced Study

- Treat the problem classically for short  $\Delta t$ ; execute on quantum hardware for  $\Delta t \gg 1$
- Use **Riemannian optimization** to incorporate the unitary constraint of the quantum gates
- Start with **fermionic swap network** that implements a Trotter step

**Classically optimize a quantum circuit to approximate the time evolution operator of a fermionic system for time** 

• Approximate the reference as a higher-order Trotterization and express it as a **matrix product operator (MPO)** 

evolution is given by two-qubit gates parametrized by  $V_{pq}$  and  $T_{pq}$ . We call the quantum gates  $\{V_i\}$  and the quantum circuit W. Furthermore, we allow for additional layers following the brickwall circuit design.

• Evaluate cost function and gradient using **tensor network methods**

#### **Quantum circuit layout and initialization**

We consider fermionic Hamiltonians of the form

$$
H = \sum_{pq} T_{pq} a_p^{\dagger} a_q + \frac{1}{2} \sum_{pq} V_{pq} n_p n_q,
$$

which covers systems like Fermi-Hubbard models or molecular Hamiltonians with diagonal interaction terms.

We initialize the model as a **fermionic swap network** [2], which implements a Trotter step using a simple brickwall circuit. E**ach orbital is represented by a qubit**. By using fermionic SWAPs, all interacting orbitals are brought next to each other once and their time

rank approximation of the original MPO. In practice, we compute the reference  $U_{\text{MPO}}$  as a high-accuracy approximation using  $4^{\text{th}}$ -order

where *C* is the norm of the reference and  $\Omega = \text{Tr}(U_{\text{MPO}}^{\dagger}W)$  is the variational part of the cost function.

[1] I. N. M. Le and C. B. Mendl. "Leveraging tensor network methods and Riemannian quantum circuit optimization for fermionic Hamiltonian simulation." In prep. [2] I. D. Kivlichan, et al. "Quantum simulation of electronic structure with linear depth and connectivity." Phys. Rev. Letters 120.11 (2018): 110501. [3] L. Causer, et al. "Scalable simulation of nonequilibrium quantum dynamics via classically optimized unitary circuits." Phys. Rev. Research 6.3 (2024): 033062.

The following shows the relative improvement from the initial 2<sup>nd</sup> order Trotterization after the optimization when using the same circuit layout (blue) and when allowing for an additional layer of two-qubit gates (red) for the Fermi-Hubbard model in 1D with 60 spin orbitals.





Trotterization and a suitable truncation ( $\varepsilon = 1e - 14$ ).

[4] A. Kotil, et al. "Riemannian quantum circuit optimization for Hamiltonian simulation." J. Phys. A 57.13 (2024): 135303.

#### **MPO representation of the reference**

To avoid the exponential scaling of the exact time evolution operator in system size, it is useful to **factorize it into smaller tensors** and express it, e.g., as an **MPO** [3]:

 $\mathcal{O}(N\chi^2d)$  $x < x$ truncation  $\times d^N$  $\approx$  $\overline{\phantom{a}}$ Id

An optional **truncation of the bond dimension**  $\chi$  results in a lower-

## **Cost function and gradient evaluation**

The deviation of the quantum circuit *W* from the reference  $U_{\text{MPO}}$  can be quantified by the normalized Frobenius norm  $\mathcal{L} = 2^{-N} \| U_{\text{MPO}} - W \|_{F}^{2} = 1 + 2^{-N} (C - 2 \Re(\Omega)),$ 

By viewing the quantum circuit as a tensor network, **the cost function is evaluated by tensor network contraction**, and similarly, **the partial** 

**derivative is obtained by "cutting out" the quantum gate**:



Following the above, the Euclidean gradient is computed.

## **Riemannian optimization of unitary quantum gates [4]**

The objective is to optimize the quantum gates under **unitary constraints** using **Riemannian optimization**. The set of two-qubit gates forms a Riemannian manifold  $u$  with a tangent space  $T_V u$  and an inner product  $\langle X, Y \rangle_V = \text{Tr}(X^{\dagger}Y)$  associated to each point  $V \in \mathcal{U}$ . The Riemannian gradient corresponds to the projection of the

Euclidean gradient onto the corresponding tangent space. Within Riemannian optimization, an update step is taken into the negative direction of the Riemannian gradient. The resulting point needs to be retracted from the tangent space into the manifold by, e.g., taking the unitary part of its polar decomposition.

## **Numerical results (preliminary)**

